

**REMARKS****Summary of the Office Action**

Claims 24, 26 and 27 stand rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the written description requirement.

Claims 21-23 and 28 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Miura et al. (U.S. Patent Application No. 2003/0087129) (hereinafter “Miura”) in view of Nishiki et al. (U.S. Patent No. 6,261,144) (hereinafter “Nishiki”).

Claims 21-23, 25 and 28 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Komada (U.S. Patent No. 2001/0038894) (hereinafter “Komada”) in view of Fumihiro (JP 11-335820) (hereinafter “Fumihiro”).

Claims 24-27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Miura in view of Nishiki, as applied to claim 21, and further in view of the allegedly “admitted prior art.”

Claims 26 and 27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Miura in view of Nishiki, as applied to claim 21, and further in view of Konishi et al. (U.S. Patent No. 5,957,743) (hereinafter “Konishi”).

Claims 24, 26 and 27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Komada in view of Fumihiro, as applied to claim 21 above, and further in view of Konishi for substantially the same reasons as applied to claims 26 and 27 above.

Claims 24-27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Miura in view of Nishiki, as applied to claim 21, and further in view of Choi et al. (U.S. Patent Application No. 2002/0063525) (hereinafter “Choi”).

Claims 24, 26 and 27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Komada in view of Fumihiro, as applied to claim 21 above, and further in view of Choi for substantially the same reasons as applied to claims 24-27 above.

**Summary of the Response to the Office Action**

Applicants have amended claims 21 and 23, and have canceled claim 28, to differently describe embodiments of the disclosure of the instant application's specification and/or to improve the form of the claims. Accordingly, claims 1-27 are currently pending with claims 21-27 currently under consideration.

**Rejection under 35 U.S.C. § 112, first paragraph**

Claims 24, 26 and 27 stand rejected under 35 U.S.C. § 112, first paragraph, as failing to meet the written description requirement. This rejection is respectfully traversed.

Specifically, Applicants respectfully submit that one skilled in the art in the field of plasma display apparatus would understand that the terms 50-size, 55-size and 60-size respectively represent a 50-inch size, 55-inch size and 60-inch size, with the size being the respective diagonal measurement. For example, a PDP having a 50-inch size in diagonal measurement is referred to as a 50-type PDP in Japan, and Sony America's PDP having a 40-inch size in diagonal measurement is referred to as a 40" BRAVIA 11.1 widescreen display in the United States as shown in Attachment-1 filed with this Amendment.

Accordingly, the Examiner is respectfully requested to withdraw this objection.

**Rejections under 35 U.S.C. § 102(e) and 103(a)**

Claims 21-23 and 28 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Miura in view of Nishiki. Claims 21-23, 25 and 28 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Komada in view of Fumihiro. Claims 24-27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Miura in view of Nishiki, as applied to claim 21, and further in view of the allegedly “admitted prior art.” Claims 26 and 27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Miura in view of Nishiki, as applied to claim 21, and further in view of Konishi. Claims 24, 26 and 27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Komada in view of Fumihiro, as applied to claim 21 above, and further in view of Konishi for substantially the same reasons as applied to claims 26 and 27 above. Claims 24-27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Miura in view of Nishiki, as applied to claim 21, and further in view of Choi. Claims 24, 26 and 27 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Komada in view of Fumihiro, as applied to claim 21 above, and further in view of Choi for substantially the same reasons as applied to claims 24-27 above. Applicants have amended claims 21 and 23 to differently describe embodiments of the disclosure of the instant application’s specification and/or to improve the form of the claims. In addition, claim 28 is being canceled. To the extent that these rejections might be deemed to apply to the claims as newly-amended, they are respectfully traversed for at least the following reasons.

The combination of features recited in independent claim 21 of the instant application is directed to a method of fabricating a plasma display panel having a display area, including the step of forming a protection film on a substrate of the plasma display panel and covering the display area. This step includes (a) feeding the substrate in a vacuum atmosphere so as to move along a passage, and (b) heating and evaporating a plurality of evaporation sources positioned to face the passage of the substrate so as to form the protection film. In addition, as recited in claim 21, at least one of the evaporation sources is positioned outside a space defined between a pair of flat planes substantially perpendicular to the passage of the substrate and passing the opposite ends of the passage of the display area. With this arrangement, a protection film having uniform crystal alignment can be formed over the entire surface of the display area, thereby improving the characteristics of the protection film, such as secondary electron emission and resistance against sputtering.

Miura discloses an apparatus for making a thin film, in which the thin film is formed on a substrate 12. The evaporation sources 14a and 14b of the apparatus are positioned somewhere below the substrate 12. However, Applicants respectfully submit that Miura fails to disclose an arrangement in which at least one of the evaporation sources 14a and 14b is positioned outside a space defined between a pair of flat planes perpendicular to the passage of the substrate 12 and passing the opposite ends of the passage of the display area.

Komada discloses an ion plating method in which a substrate 50 is displaced by winding means, and silicon oxide evaporated from a heath 306 is deposited on the substrate 50. The heath 306 is positioned somewhere below the substrate 50. However, Komada also fails to disclose an arrangement in which the heath 306 is positioned outside a space defined between a

pair of flat planes perpendicular to the passage of the substrate 50 and passing the opposite ends of the passage of the display area.

Applicants further submit that none of the other cited references make up for the deficiencies in Miura and Komada as discussed above. Accordingly, Applicants respectfully submit that one skilled in the art would not have found the embodiment of the present invention as defined in independent claim 21 obvious in view of Miura, Komada and the secondary references. Furthermore, Applicants respectfully submit that dependent claims 22-27 are allowable at least because of their dependence from newly-amended independent claim 21, and the reasons set forth above. Independent claim 28 has been canceled

### CONCLUSION

In view of the foregoing discussion, Applicants respectfully request the entry of the amendments to place the application in clear condition for allowance or, in the alternative, in better form for appeal. Should the Examiner feel that there are any issues outstanding after consideration of this response, the Examiner is invited to contact Applicants' undersigned representative to expedite prosecution. A favorable action is awaited.

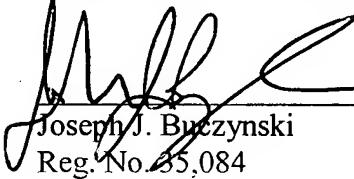
**EXCEPT** for issue fees payable under 37 C.F.R. § 1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. § 1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account No. 50-0573.

This paragraph is intended to be a **CONSTRUCTIVE PETITION FOR EXTENSION OF TIME** in accordance with 37 C.F.R. § 1.136(a)(3).

Respectfully submitted,

**DRINKER BIDDLE & REATH LLP**

Dated: April 24, 2007

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## SXRD™ Panels

SXRD technology is the latest display technology developed by the legendary television engineers at Sony to meet and exceed the demands of a high definition image at its full 1080 line resolution. Digitally transmitted HD signals can contain over 2 million individual detail points that need to be displayed accurately and rapidly. SXRD displays those 2 million detail points per SXRD panel accurately since the 3 SXRD panels actually contain enough pixels to fully display a 1080 line picture without interlacing it. SXRD has the speed to create a smooth, film like image. The SXRD panels have a blistering 2.5ms response time (total rise and fall time), which exceeds the demands of even the most rapidly moving high definition images. And SXRD creates highly accurate, natural colors because the 3-panel design displays all the colors, all the time.

## Advanced Iris

Advanced Iris is a special Sony function designed to improve brightness and contrast expression especially in darker scenes. It is accessed inside the Picture Menu and offers 2 auto settings and 5 manual settings. Auto 1 and Auto 2 are dynamic adjustments that automatically open and close the Iris according to the gamma level of the picture on the screen. Auto 1 is best used for viewing video content that has a large variation in brightness from scene to scene. Auto 2 is best for viewing video content that has less variation in brightness from scene to scene. The 5 manual settings (max, high, medium, low and min) can be used to adjust the Iris opening to allow the best possible picture viewing depending on ambient room lighting conditions. For instance, high works well for viewing in brightly lit rooms. Low is a good setting for viewing in low light conditions.

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- Green, blue and white color enhancer circuit which selectively adjusts common natural colors like green, white and blue color levels for a more vivid image.
- Digital MPEG noise reduction circuit which reduces MPEG artifacts such as "block noise" and "mosquito noise" caused by compression used in digital broadcasts used to maximize bandwidth.
- Digital contrast enhancer circuit processes every field automatically selecting out details and optimizing contrast for each scene. Adaptive processing improves the low contrast parts of the picture providing crisp and vivid images with high contrast.

### HDMI™ and PC Inputs

Designed for maximum versatility, the Grand WEGA XBR® line is equipped with a comprehensive range of input interfaces. The HDMI (High Definition Multimedia Interface) is the first industry-supported, uncompressed, all-digital audio/video interface. HDMI technology supports enhanced or high definition video, together with multi-channel digital audio to provide matchless image and sound reproduction. A D-Sub 15 input also allows connection to your PC, allowing you to use your high-resolution KDS-60A2020 TV as a computer monitor.



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